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INFORMATION DISCLOSURE			SURE	Application Number	10/781,855	
STATEMENT BY APPLICANT Date Submitted: December 7, 2005				Filing Date	February 20, 2004	
				First Named Inventor	Werner DOETSCH	
	Date Submitted	: December	r 7, 2005	Group Art Unit	1761	
	(use as many sheets as necessary)			Examiner Name	Chhaya D. Sayala	
Sheet	1	of	1	Attomey Docket Number	038715.53064US	

Offic	Foreign Patent D	ocument Kind Code ⁵ (if known)	Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Where Relevant Passages or Relevant	
				Cited Document		T ⁶
JP	62-103002	A	ISHIHARA HIRYO KOGYO KK	05-13-1987		
JP	S61-33104	А	RYO UNO	02-17-1986		×
	-			JP 62-103002 A KK	JP 62-103002 A KK	JP 62-103002 A KK

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
Cs	AC	WOLF et al., Silicon Processing for the VLSI Era, Volume 1 – Process Technology, Chapter 1, pp. 1 – 35, Lattice Press, California, USA	
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	<u> </u>		

Examiner Signature	. As	Jale	Date Considered	2/9/06

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